REMARKS

Claims 1-5 remain in the application for consideration of the Examiner.

Claims 1-5 have been amended to clarify the invention.

Turning now to the art rejections, Claims 1 and 3-5 were rejected under 35 U.S.C. §103 as being unpatentable over Codilian in view of Choi; and Claim 2 were rejected under 35 U.S.C. §103 as being unpatentable over Codilian in view of Choi and Carobolante.

These rejections are respectively traversed.

It is respectfully submitted that Codilian does not disclose or suggest the presently claimed invention including the control circuit to control the motor during at least a low voltage state, a pulse voltage state, and a high voltage state.

Codilian only discloses a two voltage state.

Choi does not disclose or suggest the presently claimed invention including the control circuit to control the motor during the low voltage, the pulse voltage state, and the high voltage state.

Choi does not disclose a three-voltage state circuit.

Whether or not Carobolante discloses the limitation of Claim 2 and whether or not one of ordinary skill in the art would consider modifying the teachings of Codilian or Choi is of no moment since the result in construction would in no way disclose or suggest the above mentioned subject matter.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "VERSION WITH MARKINGS TO SHOW CHANGES MADE."

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the claims:

Claims 1-3 and 5 have been amended as follows:

1. (Amended) A spindle motor control circuit for controlling a motor; comprising;

a control circuit to control said motor during at least a low voltage state, a pulse voltage state and a high voltage state;

said motor braking during said low voltage state;

said control circuit receiving a flyback voltage from said motor during said pulse voltage state;

said control circuit receiving a reduced flyback voltage being reduced from said flyback voltage from said motor during said high voltage state.

2. (Amended) A spindle motor control circuit for controlling a motor, as in Claim 1, wherein said control circuit includes an op amp to feed back a voltage to limit ~~cont~~ said flyback voltage from said motor.

3. (Amended) A spindle motor control circuit for controlling a motor, as in Claim 1, wherein said voltage is a first voltage during said pulse voltage state and a second voltage during said high voltage state.

5. (Amended) A spindle motor control circuit for controlling a motor, as in Claim 1, wherein said motor is braked before said pulse voltage state and after said high voltage state.